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Re: U.S. Patent Application of Rafael Reif et al.  
Application No.: 10/655,854  
Entitled: MULTI-LAYER INTEGRATED SEMICONDUCTOR STRUCTURE  
Filed On: September 5, 2003  
MIT Case No.: 10002  
DCM Case No.: MIT-136AUS

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Client Matter No.: MIT-136AUS

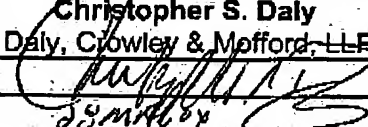
PTO/SB/21 (modified) (02-04)

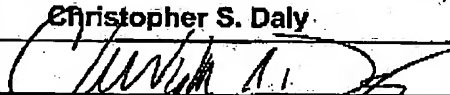
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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	10/655,854	
	Filing Date	September 5, 2003	
	First Named Inventor	Rafael Reif	
	Art Unit	2811	
	Examiner Name	Not Yet Assigned	
Total Number of Pages in This Submission	16	Attorney Docket Number	MIT-136AUS

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance communication to Technology Center (TC) <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): Return Postcard PTO SB/08A with Four (4) References Cited
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Firm or Individual name	Christopher S. Daly Daly, Crowley & Mafford, LLP	
Signature		
Date	5/28/2004	

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## PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/655,854  
Applicant : Rafael Reif et al.  
Filed : September 5, 2003  
T.C./A.U. : 2811  
Examiner : Not Yet Assigned

Confirmation No.: 7254

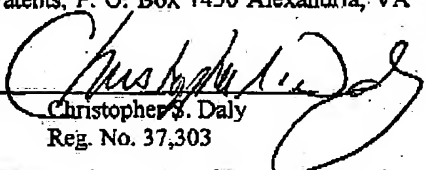
Docket No. : MIT-136AUS  
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By:

  
Christopher S. Daly  
Reg. No. 37,303

INFORMATION DISCLOSURE STATEMENT

MS Group Art Unit #2811  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

It is desired to cite for the record in this application the enclosed documents listed on the attached copy of PTO form PTO/SB/08A (Formerly #1449). The paragraph(s) marked below are applicable to this Information Disclosure Statement.

[ X ] (1) The enclosed Information Disclosure Statement is being filed: within three months of the filing date; or within three months of the entry of the national stage of the above-identified application; or before the mailing of a first Office Action on the merits; or before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. §1.114. Accordingly, Applicant(s) believes that no fee or statement is required.

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Docket No. MIT-136AUS

[ ] (2) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by the fee set forth in 37 C.F.R. § 1.17(p).

SUBMISSION UNDER 37 C.F.R. § 1.97(c)

INCLUDING A STATEMENT UNDER 37 C.F.R. § 1.97(e)(1)

[ ] (3) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by a statement under 37 C.F.R. § 1.97(e)(1). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(c)

INCLUDING A STATEMENT UNDER 37 C.F.R. § 1.97(e)(2)

[ ] (4) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by a statement under 37 C.F.R. § 1.97(e)(2). The undersigned hereby states that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual having a duty of disclosure as set forth in 37 C.F.R. § 1.56 (c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

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SUBMISSION UNDER 37 C.F.R. § 1.97(c)INCLUDING STATEMENTS UNDER 37 C.F.R. §§ 1.704(d) AND 1.97(e)(1)

[ ] (5) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by statements under 37 C.F.R. §§ 1.704(d) and 1.97(e)(1). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement and was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of the accompanying Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(d)INCLUDING A STATEMENT UNDER 37 C.F.R. §1.97(e)(1)

[ ] (6) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by a statement under 37 C.F.R. § 1.97(e)(1) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(d)INCLUDING A STATEMENT UNDER 37 C.F.R. §1.97(e)(2)

[ ] (7) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by a statement under 37 C.F.R. § 1.97(e)(2) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to

Appl. No. 10/655,854

Docket No. MIT-136AUS

any individual having a duty of disclosure as set forth in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(d)

INCLUDING STATEMENTS UNDER 37 C.F.R. §§ 1.704(d) AND 1.97(e)(1)

[ ] (8) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by statements under 37 C.F.R. §§ 1.704(d) and 1.97(e)(1) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement and was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of the accompanying Information Disclosure Statement.

The filing of this Information Disclosure Statement is not a representation by the undersigned as to personal knowledge of the contents of every word or phrase of the material enclosed or that reliance on other suitably trained professionals has not been made.

If a search report of a searching agency is enclosed identifying the nature of the relevance of each document, such a designation is deemed to satisfy Rule 98(a) (3) even if in a foreign language, since the few terms of relevance therein are deemed of universal cognizance. However, Applicant(s) does not necessarily adopt the position reflected by that report.

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Docket No. MIT-136AUS

The Commissioner is hereby authorized to charge payment of any additional fees associated with this communication or credit any overpayment to Deposit Account No. 500845.

Respectfully submitted,

Dated: 28 May 04

DALY, CROWLEY & MOFFORD, LLP

By: 

Christopher S. Daly

Reg. No. 37,303

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PTO/SB/08B (02-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Complete if Known**

Application Number	10/655,854
Filing Date	September 5, 2003
First Named Inventor	Rafael Reif
Art Unit	2811
Examiner Name	Not Yet Assigned
Attorney Docket Number	MIT-136AUS

Sheet 1 of 1

**OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		CHRIS GAITHER, Intel Set To Unveil Advanced Communications Chip, The Boston Globe, October 15, 2002, Pages 1-3, Boston, Massachusetts	
		PETER CLARKE, IEDM Late Paper: IBM Shows How to Stack for 3D Circuitry, Silicon Strategies.Com, November 28, 2002, Pages 1-2, LONDON	
		TRU-SI TECHNOLOGIES, Through-Silicon Vias, Tru-Si Technologies Home Page, April 7, 2003, Page 1, Sunnyvale, California	
		MARK LAPEDUS, Tezzaron Devises Multi-Wafer Stack for 3-D Chips, Semiconductors Business News, June 30, 2003, Pages 1-3, Naperville, Illinois	

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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